Bela - Bug #1537
Digital/Analog loopback test randomly fails for -p2

2016-01-26 12:05 AM - Giulio Moro

Status:	New	**Start date:**	2016-01-26
Priority:	Low	**Due date:**	
Assignee:		**% Done:**	0%
Category:		**Estimated time:**	0.00 hour
Target version:			

Description

When the buffer size is 2 (-p2), this randomly fails in that the first loopback sample received is not the expected one. Maybe some stale memory somewhere? Or the DAC itself? Regardless of the number of analog channels in use

History

#1 - 2016-02-02 04:00 PM - Giulio Moro

- Status changed from New to Rejected

It is expected behaviour, as the first two analog in samples of the first buffer that comes in are actually sampled BEFORE the first two GPIO or analog outputs are written, so their value, for the purpose of this test, is undefined.

#2 - 2016-02-02 10:05 PM - Giulio Moro

- Status changed from Rejected to New

It does not fail on the first loopback buffer. Rather, each of the paragraphs below is an example of failure

D expected: 1, received: 0 0, pointer: 0, delay: -1, count: 6

D expected: 1, received: 0 0, pointer: 0, delay: -1, count: 6
expected: 1, received: 0 0, pointer: 1, delay: 0, count: 8

D expected: 0, received: 0 1, pointer: 0, delay: 0, count: 4